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To: Examiner Hsien-Ming Lee  
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 From: Timothy M. Honeycutt  
 Client/Matter: Serial No.: 10/085,903 (Atty. Dkt. No. AMDI:115\HON)  
 Date: April 15, 2004

DOCUMENTS	NUMBER OF PAGES*
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## COMMENTS:

The above-listed papers are transmitted herewith for filing.

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Andy Wei et al.

§ Group No.: 2823

Serial No.: 10/085,903

§

Filed: February 28, 2002

§ Examiner: Hsien Ming Lee

For: SOI MOSFET JUNCTION  
DEGRADATION USING MULTIPLE  
BURIED AMORPHOUS LAYERS

§ Atty. Dkt. No.: AMDI:115HON

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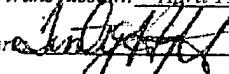
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

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**CERTIFICATE OF FACSIMILE TRANSMISSION**  
37 C.F.R. § 1.6(d)

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office (Fax No. (703) 872-9306), on the date below:

Date of transmission: April 15, 2004

Signature: 

Printed Name: Timothy M. Honeycutt

**SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT PURSUANT TO 37 C.F.R. 1.97(c) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. 1.56(a), Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and that the reference(s) listed in the Supplemental form PTO-1449 filed concurrently herewith be considered by the Examiner and made of record.

In accordance with 37 C.F.R. 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information cited in the accompanying Supplemental form PTO-1449 is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. 1.56(b), exists.

The information is listed in the accompanying Supplemental form PTO-1449 in accordance with 37 C.F.R. 1.98. Any explanation of non-English language documents cited in the accompanying form PTO-1449 is believed to constitute a concise explanation of the relevance of the listed reference(s) as they are presently understood by the individual designated in 37 C.F.R. 1.56(c) most knowledgeable about the content of the listed reference(s), in accordance with 37 C.F.R. 1.98(a)(3).

Serial No.: 10/085,903

The Commissioner is authorized to charge any required fees or credit any overpayment to Deposit Account No. 01-0365, Order No. AMDI:115\HON.

Respectfully submitted,

Date:

April 15, 2004



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<b>Supp. Form PTO-1449 (modified)</b>  List of Patents and Publications For Applicant's Information Disclosure Statement  (Use several sheets if necessary)		ATTY. DOCKET NO. AMDI:115HON	SERIAL NO. 10/085,903
<b>APPLICANT</b> Andy Wei et al.		<b>CERTIFICATE OF FACSIMILE TRANSMISSION</b> <i>37 C.F.R. § 1.6(d)</i> I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office (Fax No. (703) 872-9306), on the date below: <i>Date of transmission: April 15, 2004</i> <i>Signature: </i> <i>Printed Name: Timothy M. Honeycutt</i>	
<b>FILING DATE</b> February 28, 2002		<b>GROUP</b> 2823	

**U.S. PATENT DOCUMENTS**

EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION

**OTHER ART**

EXAM INIT.	REF. DES.	AUTHOR; TITLE; JOURNAL; VOLUME; DATE; PERTINENT PAGES
	A1	Stanley Wolf and Richard N. Tauber; <i>Silicon Processing for the VLSI Era, Volume 2 - Process Integration</i> ; 1990; pp. 298-299

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*Information Disclosure Statement--PTO-1449 (Modified)*